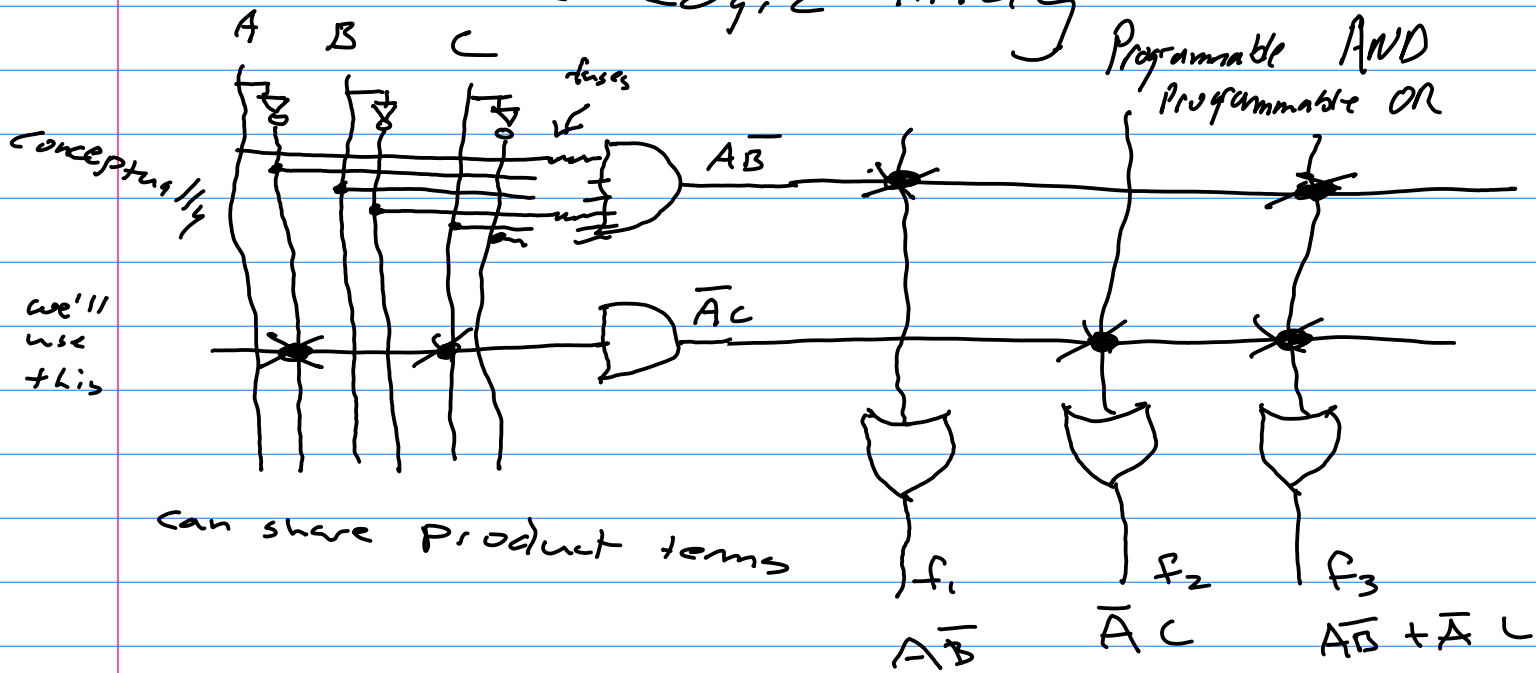
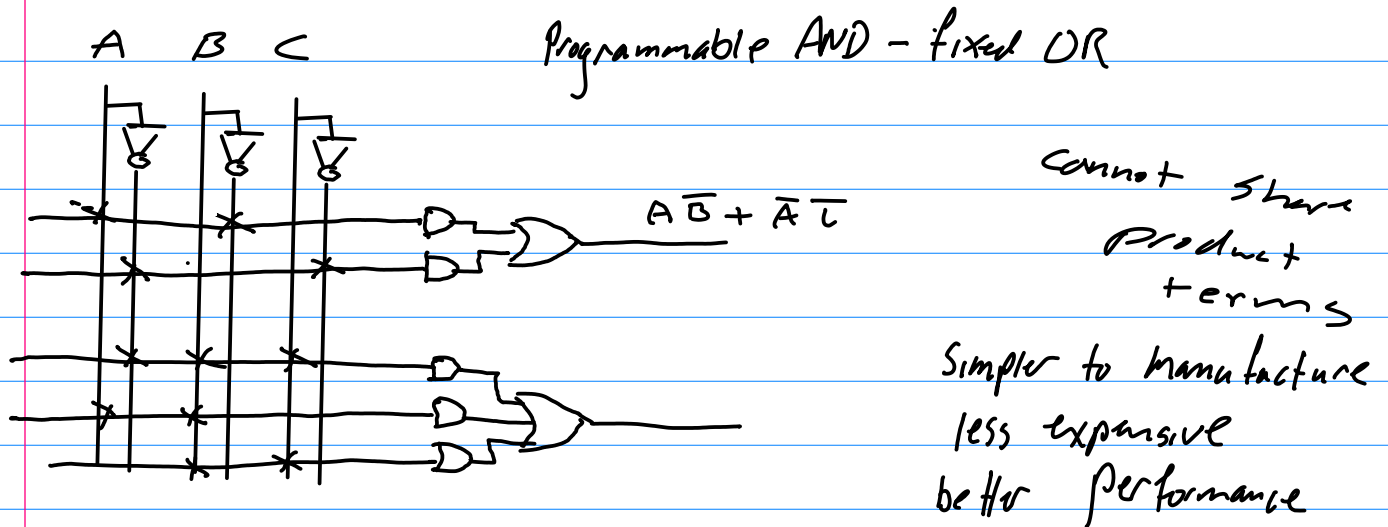


Programmable Logic Devices (PLD)

PLA - Programmable Logic Array

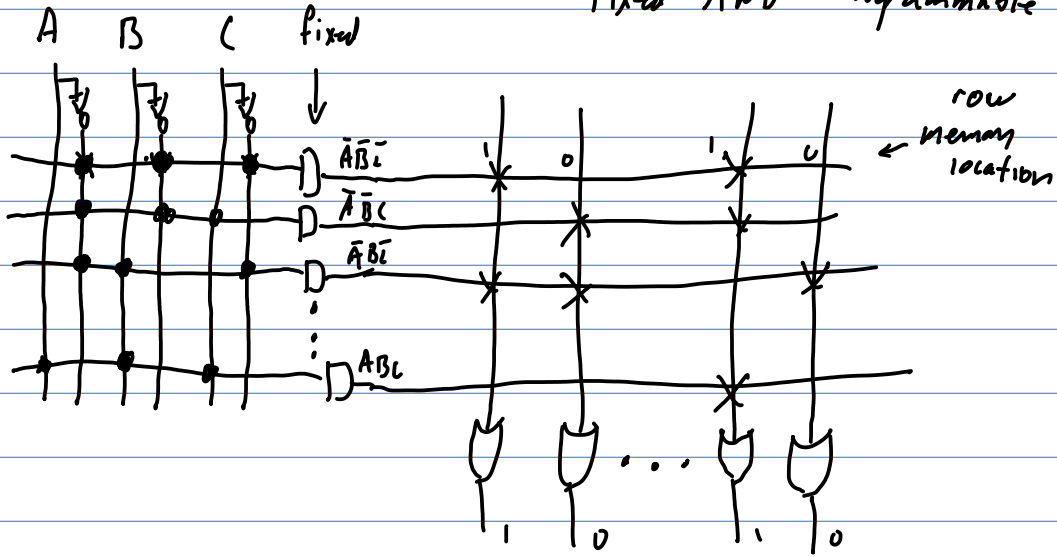


PAL - Programmable Array Logic

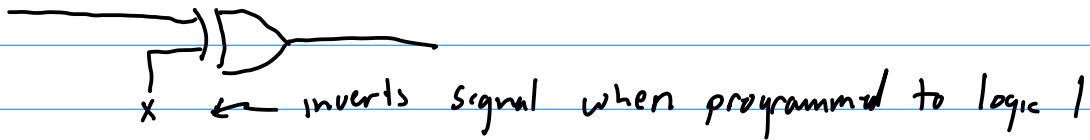
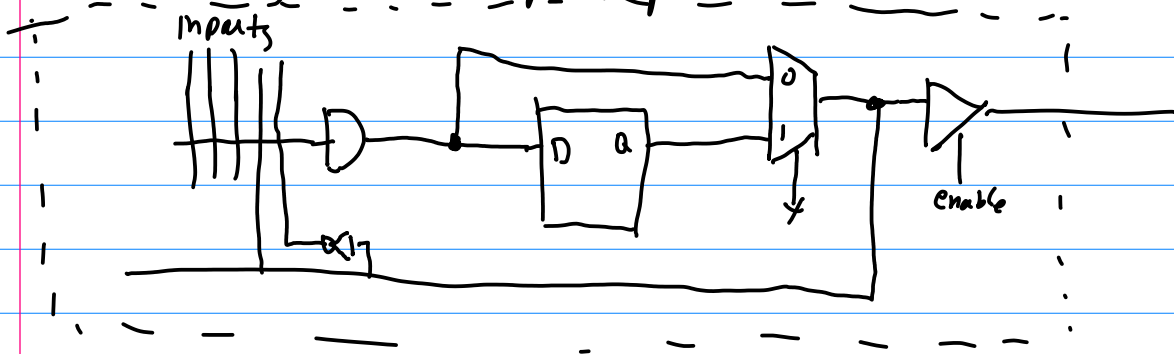


EEPROM - Programmable Read-only memory

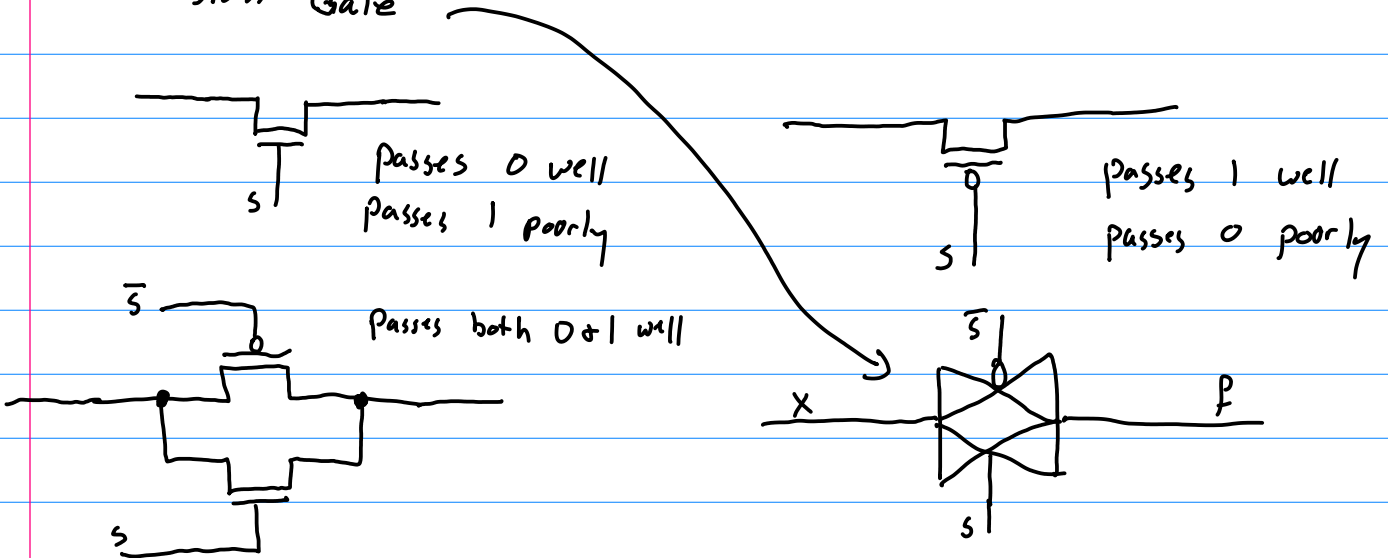
Fixed AND - Programmable OR



macro cells - add flip-flop



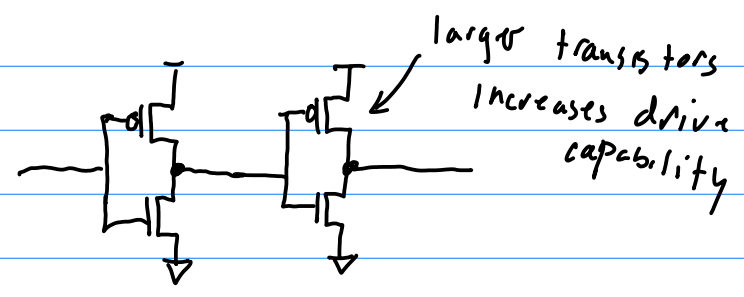
Transmission Gate



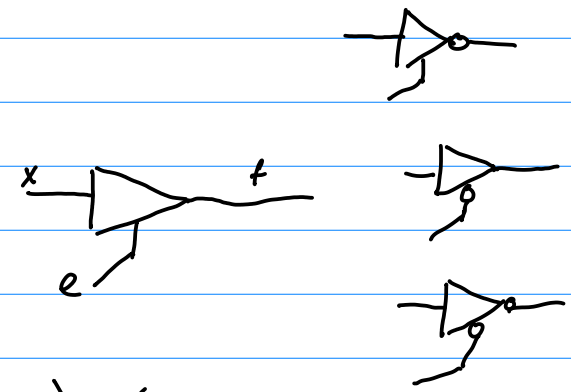
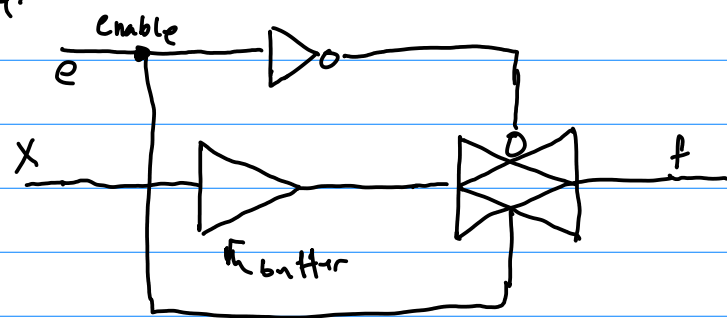
Buffer non-inverting



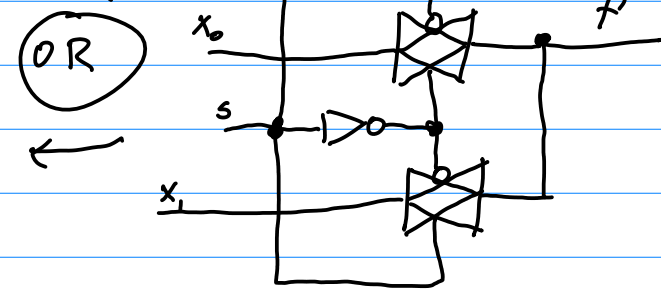
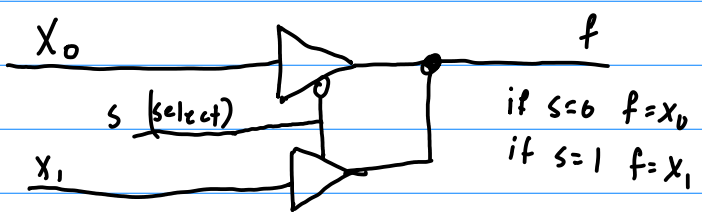
Inverting



Tri-state buffer



Multiplexor



logically $f = \bar{s}X_0 + sX_1$

