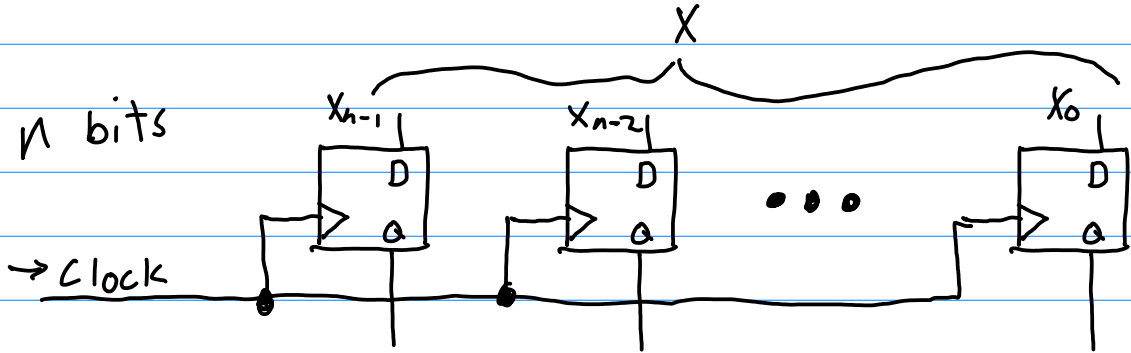
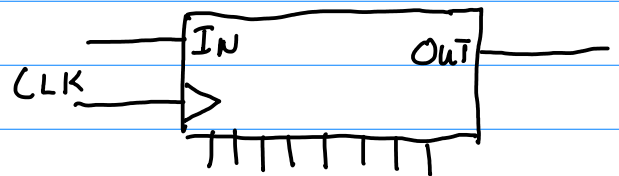
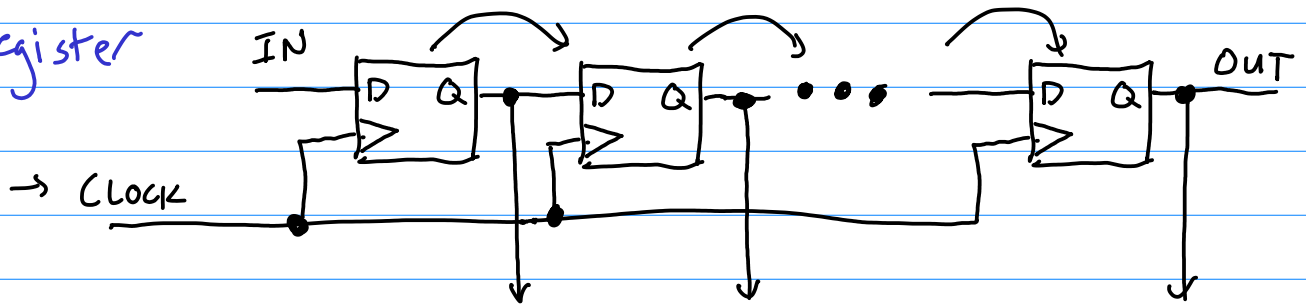


Some standard arrangements:

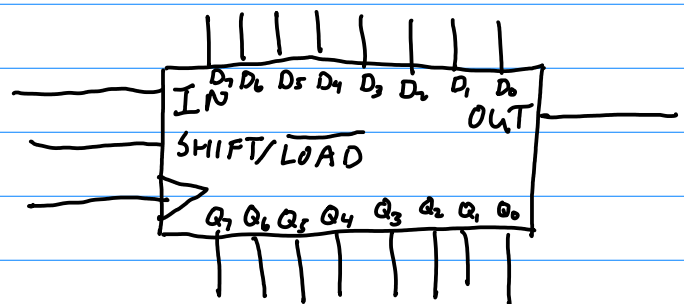
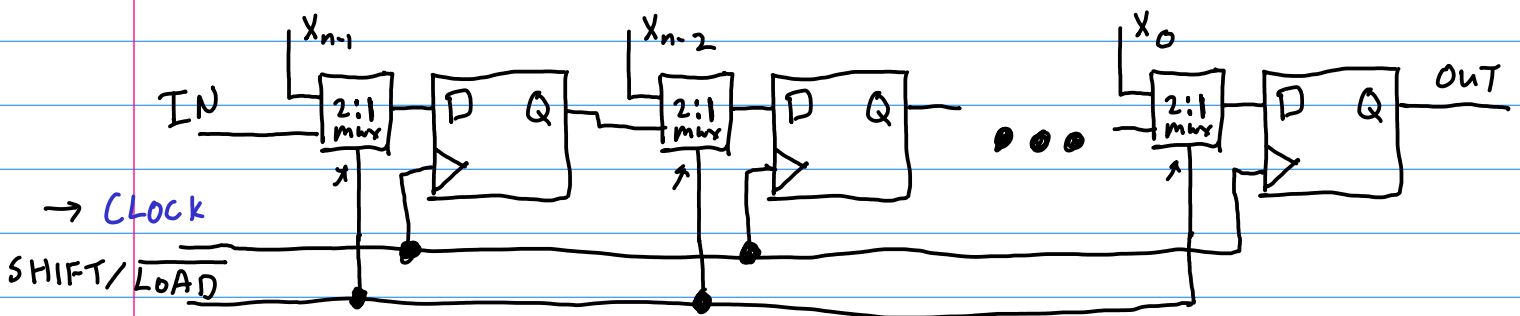
Register - an array of flip-flops



Shift register

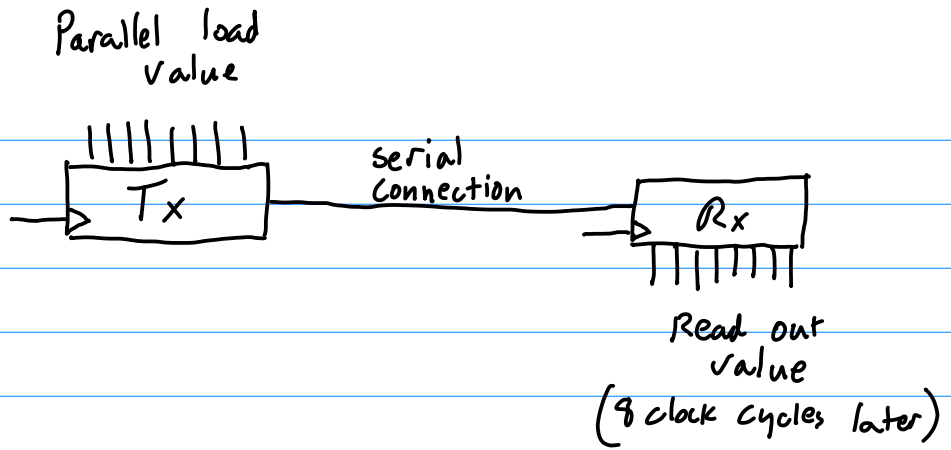


Parallel Access on inputs:



Serial Communication

Example



Can also do:

- | | |
|------------|-------------|
| c_i, c_o | load |
| 00 | hold |
| 01 | Shift right |
| 10 | Shift left |
| 11 | |

