

Errata of
Embedded Systems with ARM Cortex-M Microcontrollers in Assembly Language and C
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Thank you all for providing me feedbacks and corrections!

Chapter 1. See a Program Running

Chapter 2. Data Representation

Chapter 3. ARM Instruction Set Architecture

Chapter 4. Arithmetic and Logic

Chapter 5. Load and Store

Chapter 6. Branch and Conditional Execution

Chapter 7. Structured Programming

Chapter 8. Subroutines

Chapter 9. 64-bit Data Processing

Chapter 10. Mixing C and Assembly

Chapter 11. Interrupt

Chapter 12. Fixed-point and Floating-point Arithmetic

Chapter 13. Instruction Encoding and Decoding

Chapter 14. Generic-purpose I/O

Chapter 15. General-purpose Timers

Chapter 16. Stepper Motor Control

Chapter 17. Liquid-crystal Display (LCD)

Chapter 18. Real-time Clock (RTC)

Chapter 19. Direct Memory Access (DMA)

Chapter 20. Analog-to-Digital Converter

Chapter 21. Digital-to-Analog Converter

Chapter 22. Serial Communication Protocols

- Page 576, in Example 22-27, Send data to an SPI slave
 1. `SPIx->DR = txBuffer[i];`
should be: `*((volatile uint8_t*)&SPIx->DR) = txBuffer[i];`
 2. `rxBuffer[i] = SPIx->DR;`
should be: `rxBuffer[i] = *((volatile uint8_t*)&SPIx->DR);`
- Page 577, in Example 22-28, Receive data from an SPI slave
 1. `SPIx->DR = 0xFF; // A dummy byte`
should be: `should be: *((volatile uint8_t*)&SPIx->DR) = 0xFF`
 2. `rxBuffer[i] = SPIx->DR;`
should be: `rxBuffer[i] = *((volatile uint8_t*)&SPIx->DR);`

Chapter 23. Multitasking

Chapter 24. Digital Signal Processing