# ECE 571 – Advanced Microprocessor-Based Design Lecture 8

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# Hardware Performance Counters – Software Tools



# PAPI (Performance API)

- Low-level Performance Measurement Interface
- Cross-platform
- Self-monitoring or Sampling
- C, C++, Fortran (or attach to running process)
- Basis for more advanced visualization tools. Vampir, Tau, PerfExpert, etc.



- Provides high-level access to timers
- Provides high and low-level access to performance counters
- Provides profiling support
- Provides system information
- Components
- Fine-grained instrumentation



#### **PAPI** Limitations

- Limitations: In general have to modify source code
- Limitation: Overhead included in program run



#### **PAPI** Platforms

- Linux perf\_event
- Linux perfmon2/perfctr (mostly deprecated except Cray)
- IBM BlueGene P/Q
- Solaris
- FreeBSD
- IBM AIX



#### **PAPI CPUs**

- x86, MIC
- ARM
- Power
- SPARC
- Itanium
- MIPS



# **PAPI** Components

- Appio I/O bandwidth
- BGPM IBM Bluegene extra
- Coretemp chip temp sensors, etc.
- CUDA NVidia GPU
- Infiniband high-speed network
- Imsensors chip sensors



- lustre parallel filesystem
- micpower power on Intel MIC (Xeon PHI)
- MX myrinet, high-speed network
- net generic Linux network
- NVML Nvidia power
- RAPL Intel Sandybridge/Ivybridge Power
- Stealtime Virtual Machine stealtime



VMware – VMware stats



#### **PAPI Tools**

Note, unlike perf PAPI is rarely installed by default.

- papi\_component\_avail list all components on system
- papi\_avail list all predefined events
- papi\_native\_avail list all native events



#### **PAPI** Instrumentation

Code has to be instrumented and linked against PAPI library.

Usually this is done manually, but some tools can do this automatically via binary instrumentation.



#### **PAPI Timers**

```
#include "papi.h"
int main(int argc, char **argv) {
      int retval;
      long long start_real_usecs, end_real_usecs;
      long long start_virt_usecs, end_virt_usecs;
      retval = PAPI_library_init(PAPI_VER_CURRENT);
      if (retval != PAPI_VER_CURRENT) {
         fprintf(stderr, "Wrong PAPI version n");
      start_real_usecs = PAPI_get_real_usec();
      start_virt_usecs = PAPI_get_virt_usec();
      naive_matrix_multiply(0);
```





# PAPI\_get\_real\_usec() vs PAPI\_get\_virt\_usec()

- PAPI\_get\_real\_usec()
   wall-clock time
   maps to clock\_gettime(CLOCK\_REALTIME)
- PAPI\_get\_virt\_usec()
   only time process is actually running
   maps to clock\_gettime(CLOCK\_THREAD\_CPUTIME\_ID)



# Measuring Predefined Event

- We'll use the PAPI\_TOT\_INS pre-defined counter
- On Sandybridge this maps to INSTRUCTION\_RETIRED
- Currently PAPI can have more elaborate pre-defined events than perf (can do linear combinations, etc).



#### PAPI\_TOT\_INS Measurement

```
#include "papi.h"
int main(int argc, char **argv) {
      int retval, event_set=PAPI_NULL;
      long long count;
      retval = PAPI_library_init(PAPI_VER_CURRENT);
      if (retval != PAPI_VER_CURRENT)
         fprintf(stderr, "Wrong PAPI version n");
      retval = PAPI_create_eventset( &event_set);
      if (retval != PAPI_OK)
         fprintf(stderr, "Error creating eventset n");
      retval = PAPI_add_named_event( event_set,
                                      "PAPI_TOT_INS" );
```



```
if (retval != PAPI_OK)
    fprintf(stderr, "Error_adding_event\n");
retval = PAPI_start(event_set);

naive_matrix_multiply(0);

retval = PAPI_stop(event_set,&count);
printf("Total_instructions:_\%lld\n",count);

return 0;
```



#### Results

```
vince@vincent-weaver-1:~/class$ ./matrix_multiply.papi
```

Matrix multiply sum: s=27665734022509.746094

Total instructions: 945573824



#### **PAPI Overflow**

 PAPI Can do overflow, but only provides RAW Program Counter

Need external tool if want more detailed info



# Performance Measurement Methodologies

- Aggregate Count overall, total counts
- Profiling measure at beginning of function.
   gprof, Valgrind Callgrind.
   records every entry/exit into a function, knows full backtrace
- Statistical Sampling samples at rate of some sort of periodic counter perf record, oprofile



can completely miss important functions if unlucky, harder to get backtrace



#### Inclusive vs Exclusive

- Exclusive only lists time spent in actual function
- Inclusive includes time spent in all child functions



#### **Performance Counters and Java**

This was asked about at the end of the last class. Here are the results of a quick literature search, not very thorough.



# Sweeny et al. USENIX VM 2004

Using Hardware Performance Monitors to Understand the Behavior of Java Applications by Sweeney, Hauswirth, Cahoon, Cheng, Diwan, Grove, and Hind (USENIX VM 2004).

#### They found these challenges:

- Distinguish application instructions from VM instructions
- Handling threads
- Capture time-varying behavior of thread
- Handle threads migrating across CPUs



# Sweeny et al. – continued

- They Modify the Jikes VM to generate thread traces on AIX.
- They gather per results with Pmapi and the SPECjbb2000 Benchmarks
- They add native methods to access counters and add code to save/restore counters on thread switch.
- This has an Overhead of 2%



# Sweeny et al. – continued

#### Their results:

- Found IPC improvement over time as optimizer worked
- IPC got worse after garbage collection. Turns out compacting memory destroys cache.



# Forst, Eclipsecon 2008

Analyzing Java Performance Using Hardware Performance Counters by Gary Frost (Eclipsecon 2008).

- hprof takes you to the method but not into the method.
- AMD CodeAnalyst. Uses HW perf counters, integrates with Eclipse. Allows mapping up with actual lines in Java code. Unclear exactly how this works (just a slide deck, not a paper).



#### Schneider and Gross – LCPC2005

Using Platform-Specific Performance Counters for Dynamic Compilation Schneider and Gross, LCPC2005.

- VM/JIT. Plan to use counters to help optimize JIT.
- Use custom kernel module, Pentium 4
- Table of methods, list of where each byte code starts.
   Once get address have to do binary search to find which byte code it matches.



# Georges et al. – OOPSLA 2007

Statistically Rigorous Java Performance Evaluation by Georges, Buytaert, Eeckhout (Oopsla07).

Sources of non-determinism in Java Programs:

- JIT compilation (timer-based sampling might lead to different JIT invocation times)
- Thread scheduling
- Garbage collection
- System interrupts



# Georges et al. – continued

- Problem reporting "best" run versus 95% confidence interval
- They found it changes conclusions.
- Methodology is important



# **Counter Determinism**



#### **Uses of Counter Determinism**

- Validating simulators
- Generating Basic Block Vectors
- Performing Feedback-Directed Optimization
- Hardware Checkpointing/Rollback
- Intrusion Analysis
- Parallel Deterministic Execution (Deterministic Locking)



#### Determinism vs Overcount

- Determinism same count every time you run
- Overcount an event counts more than the expected amount



#### **HW Sources of Non-Determinism**

- Operating-System interaction
- Program Layout
- Measurement Overhead
- Multi-processor variation
- Hardware Issues



#### **SW Sources of Non-Determinism**

- Accessing changing values, such as time
- Pointer-value dependencies



#### Problems fount on x86\_64

- Hardware Interrupt Interference extra counts due to HW interrupts. This includes page faults.
- Instruction Overcounts some counters are buggy and count extra
- FP exception/Lazy FP Handling
- Instructions that count uops rather than instructions



#### **x86** Deterministic Counters

Core2: Retired Stores

• Westmere, SNB, IVB: Conditional Branches



# Mytkowicz et al. ASPLOS 2009

Producing Wrong Data Without Doing Anything Obviously Wrong. Mytkowicz, Diwan, Hauswirth, Sweeney: ASPLOS 2009.

- Measurement bias
- Simple experiment, seeing if −03 compiler optimization is better not straightforward
- Changing environment variable size affects cycle count.



- Changing link order can change performance more than compiler optimization
- It varies by machine and by compiler



# **Counter Accuracy Conclusion**

Results are not meant to scare you or say to not use counters.

Just remember they are not perfect and keep in mind their limitations as you use them.

