

**ECE571: Advanced Microprocessor Design – Homework 7**  
Spring 2016

**Due: Thursday 31 March 2016, 3:30PM**

Create a document that contains the answers to the questions below. A .pdf or .txt file is preferred but I can accept MS Office or Libreoffice format if necessary.

1. Read the website article:

**The Intel Skylake Mobile and Desktop Launch, with Architecture Analysis** by Ian Cutress (note that there are 8 pages to this article).

<http://anandtech.com/show/9582/intel-skylake-mobile-desktop-launch-architecture-analysis/>

2. Answer the following questions:

- (a) List two new changes in the skylake processor that reduce the power usage of the chip.
- (b) Why did they move the FIVR (integrated voltage regulator) back off chip again?
- (c) What is the purpose of the Intel Speed Shift technology?

3. Submitting your work.

- Create the document containing the answers to the questions asked.
- Please make sure your name appears in the document.
- e-mail the file to me by the homework deadline.