

ECE571: Advanced Microprocessor Design – Homework 11
Spring 2018

Due: Thursday 26 April 2018, 3:30pm

Create a document that contains the answers to the questions below. A .pdf or .txt file is preferred but I can accept MS Office or Libreoffice format if necessary.

1. Read the following article:

- **Volta: Performance and Programmability** by Choquette, Giroux, and Foley
<https://ieeexplore.ieee.org/document/8344474/>
- You can download this through the UMaine library's subscription to IEEE explore, as done in an earlier homework.
- Answer the following questions:
 - (a) What is NVLink?
 - (b) What is a power saving feature available on Volta?
 - (c) What are tensor cores primarily used for?

2. Read the following website article:

- **NVIDIA Announces Jetson TX1 – A Tegra X1 Module & Development Kit**
by Ryan Smith
<http://www.anandtech.com/show/9779/nvidia-announces-jetson-tx1-tegra-x1-module-development-kit>
- Answer the following questions:
 - (a) What's one benefit of the Tegra TX-1 over the previous TK-1 version?

3. Submitting your work.

- Create the document containing the answers to the questions asked.
- Please make sure your name appears in the document.
- e-mail the file to me by the homework deadline.